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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Marc E. Goldfarb, Edmund J. Balboni

Title: REFERENCE SIGNAL GENERATORS

Mail Stop Patent Application  
Commissioner for Patents  
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Alexandria, Virginia 22313-1450

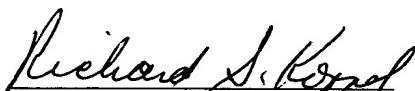
**INFORMATION DISCLOSURE STATEMENT**

Sir:

Enclosed is PTO Form 1449 (and copies of non-U.S. references) for this application. The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

Respectfully submitted,



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April 15, 2004

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SRI\ADIV\A5WI2646US\Info Disclosure Statement

Sheet \_\_\_\_ of \_\_\_\_

Form PTO-1449 <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b> <i>(Use several sheets if necessary)</i>							
				Docket Number (Optional) A5WI2646US		Application Number	
				Applicant Goldfarb, Marc E.			
				Filing Date		Group Art Unit	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	DOCUMENT NUMBER		DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	4 8 3 3 4 2 7	5/23/89	Leuthold, et al.	331	165		
	4 9 9 9 5 8 8	3/12/91	Koch	331	109		
	5 0 3 4 7 0 6	7/23/91	Betti, et al.	331	117		
	6 0 6 4 2 7 7	5/16/00	Gilbert	331	117		
	6 0 9 1 3 0 7	7/18/00	Nelson	331	109		
	6 2 7 8 3 3 8	8/21/01	Jansson	331	116		
<b>FOREIGN PATENT DOCUMENTS</b>							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
	Harvey, Barry, "High-Purity Sinewave Oscillators with Amplitude Stabilization", Elantec High Performance Analog Integrated Circuits, Application Note 5, pp 1-4						
	Shin, Hyunchol, et al., "Intermediate-Frequency/Baseband Analog Processing Chipset for a CDMA/AMPS Mobile Handset", Journal of the Korean Physical Society, Vol. 39, No. 1, July, 2001, pp 160-164						
	Vittoz, Eric, et al., "CMOS Analog Integrated Circuits Based on Weak Inversion Operation", IEEE Journal of Solid-State Circuits, Vol. SC-12, No. 3, June 1977, pp. 224-231						
EXAMINER				DATE CONSIDERED			
<b>EXAMINER:</b> Initial if citation considered, whether or not citation is in conformance with MPEP§609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.							